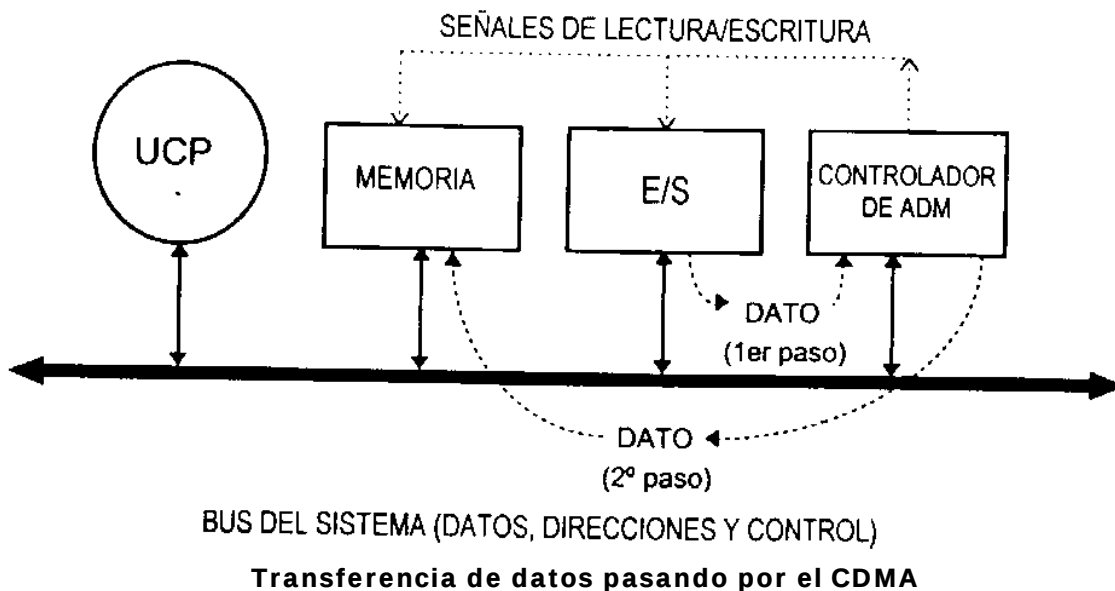
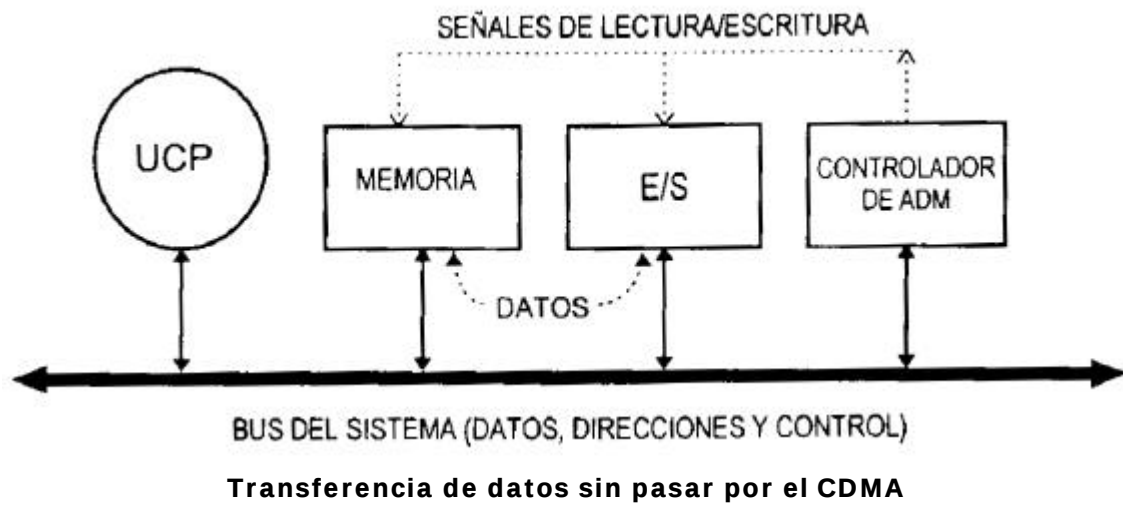
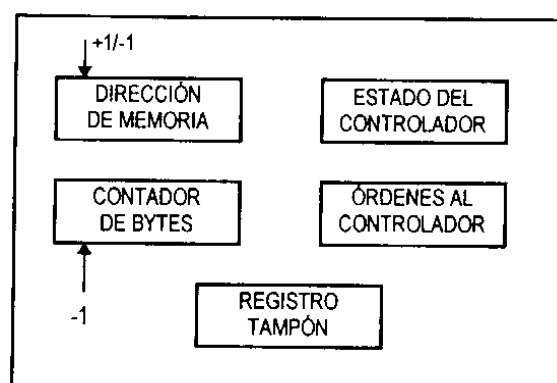


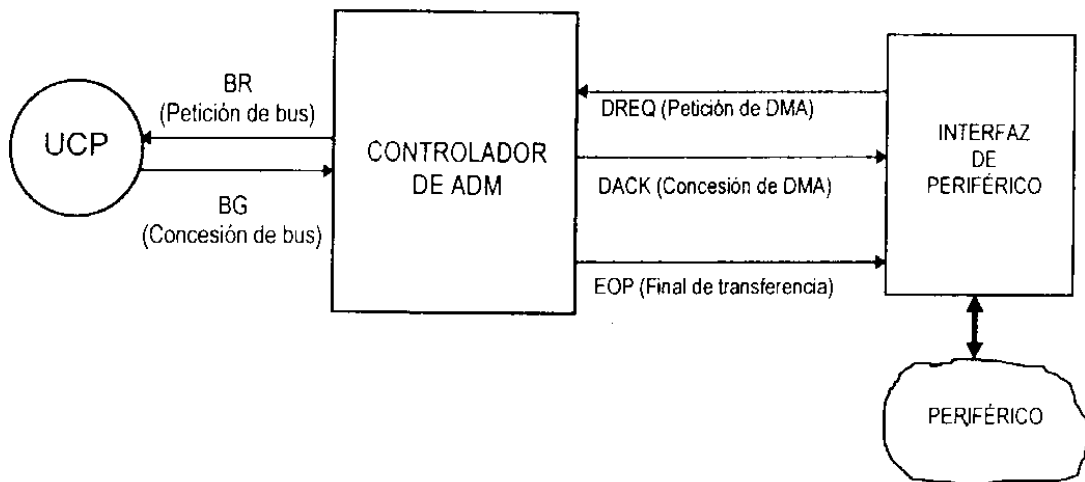
5.2.- CONTROLADOR DE DMA



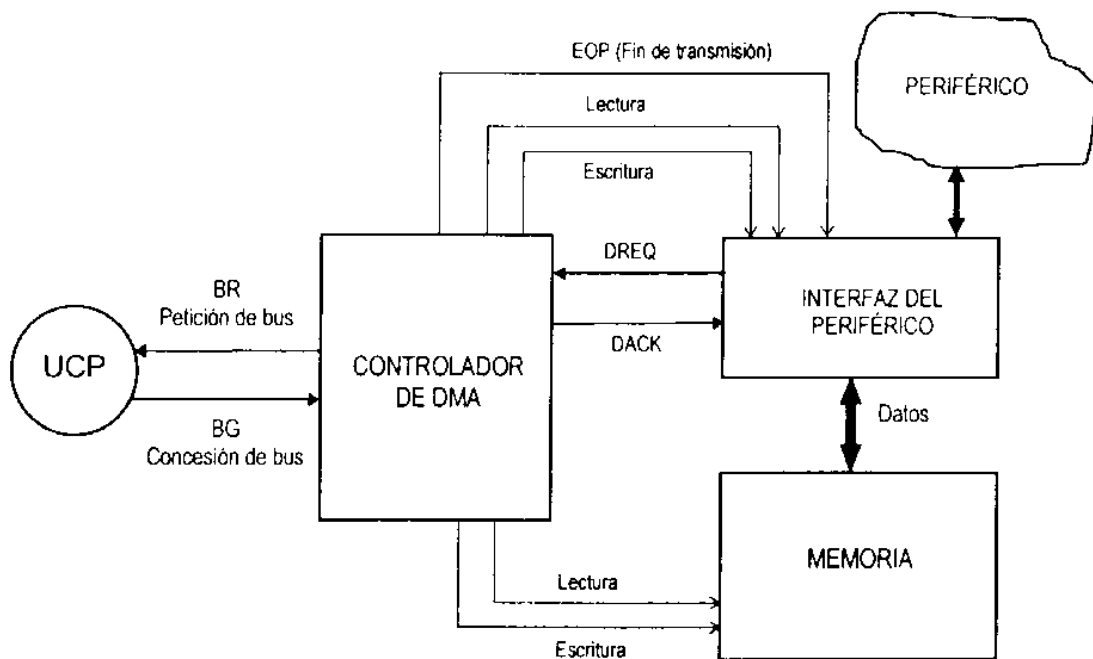
5.2.1.- Diagrama de bloques



5.2.2.- Funcionamiento



Conexión del CDMA al microprocesador



Conexión del CDMA al periférico y a la memoria