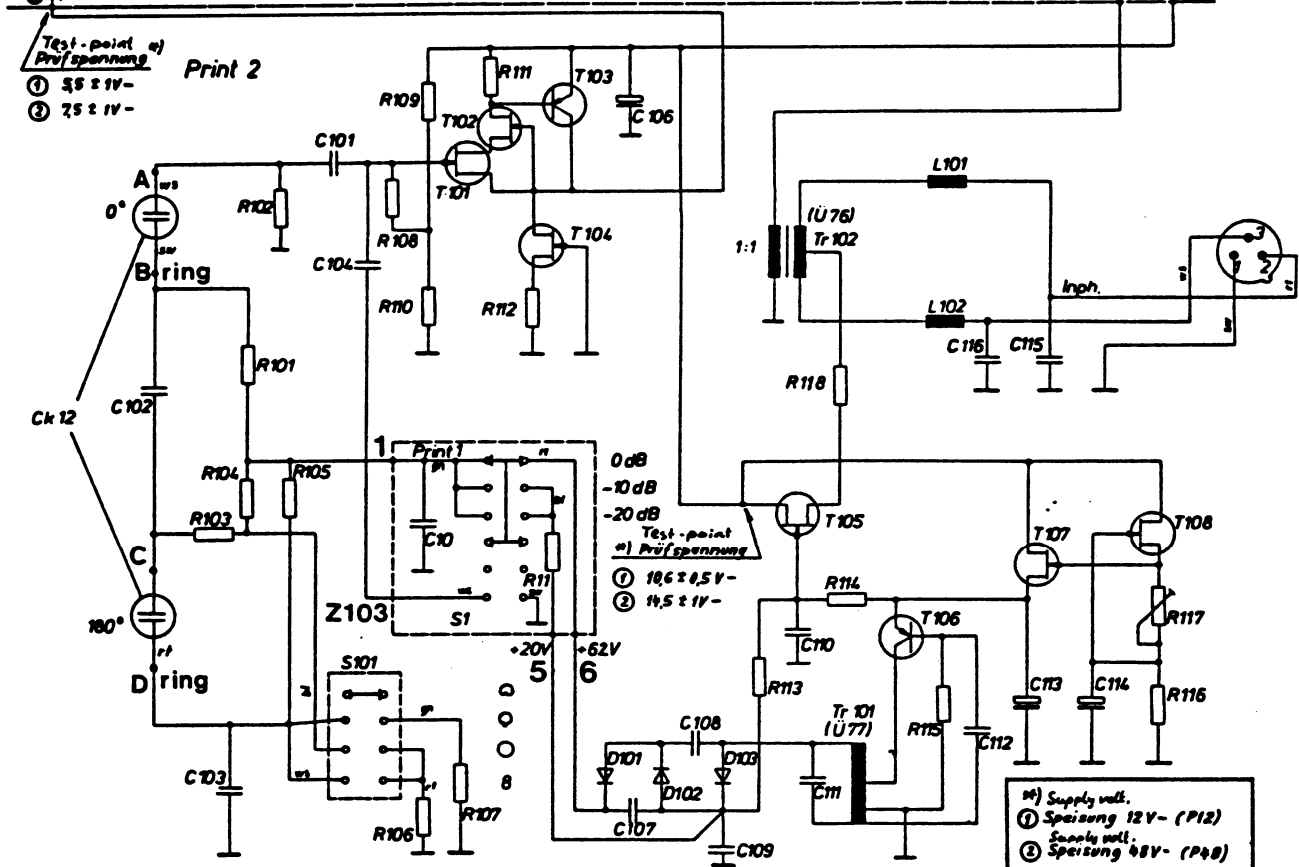
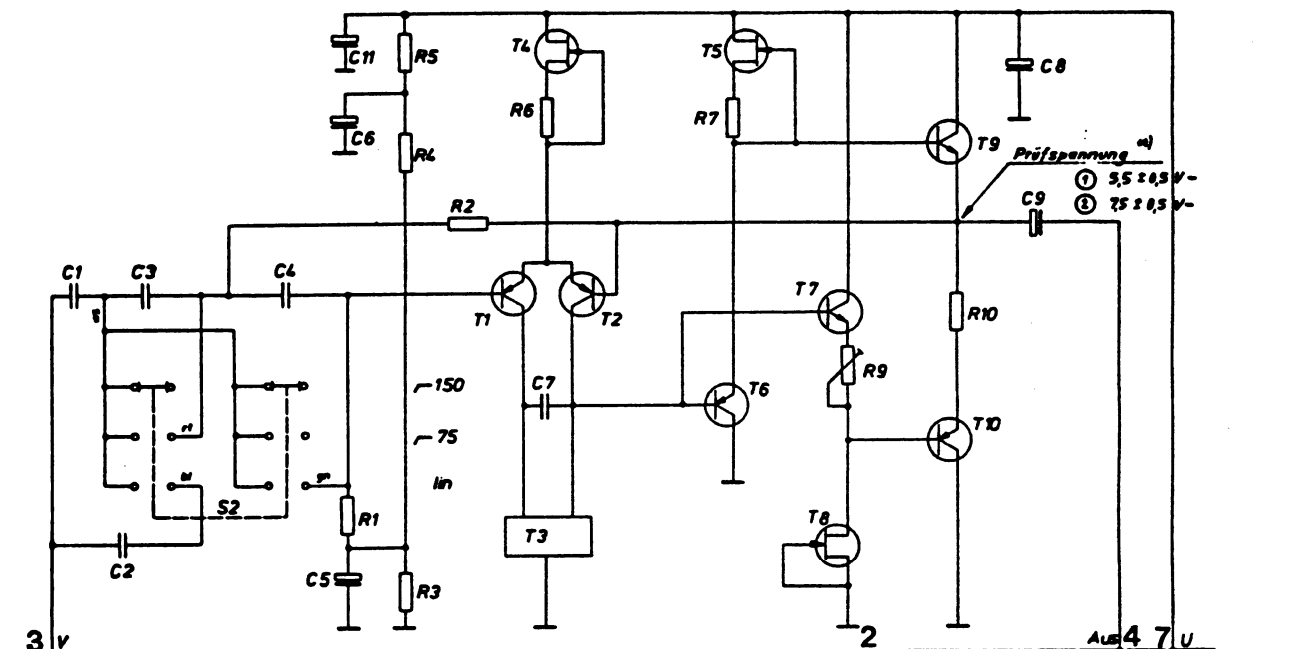
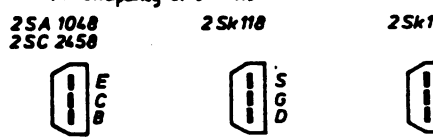




Anschlußbelegungen der Transistoren
Pin occupancy of transistors



(Lötseite)
(bottom view)

